

Course Information Sheet – ETEC 352  
***Electronics for Engineering Technology II – Winter, 2006***

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Grades: Homework and Quizzes (25), Labs (25), Tests and Final Exam (50)

Text: *Circuit Analysis with Devices* by Robbins & Miller

The material we will cover this quarter includes the following:

Transistor Fundamentals  
The Ideal Amplifier  
Logic Gates  
Transducers  
Ladder (relay) Logic

Several of these topics are not covered by the textbook. You will rely on notes, handouts, and the web for resource material.

Please strive to submit homework assignments and laboratory reports on schedule so that I can grade and return them in a timely manner. Please identify each homework assignment submitted by clearly showing the chapter and problem set span at the top of the first page. Laboratory reports should have a cover page, an introduction, and a brief description of the procedure. The results must be tabulated with a column devoted to reporting the percent differences between predicted and measured results. Your conclusions must convince the reader that learning took place. Laboratory grades will be based on technical accuracy, professional presentation, and proper use of the English language. Original data and any laboratory handouts **MUST** be attached as the last pages of the report.

You will present a project during the final week of the quarter based on combinational or sequential logic. Your project may be an individual one or you may choose to work with a partner. More details will be provided soon.